

FIG. 1

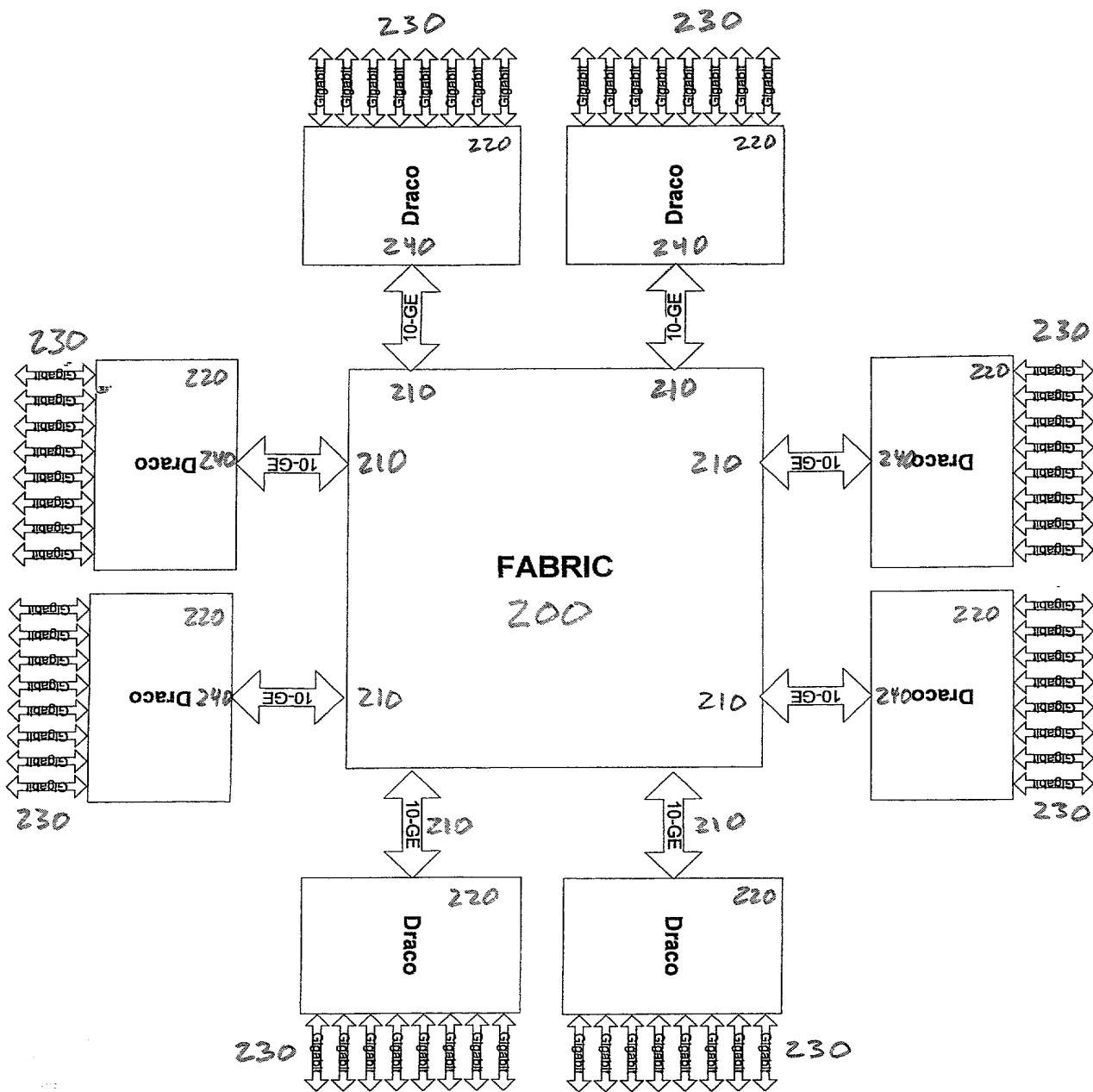


FIG. 2

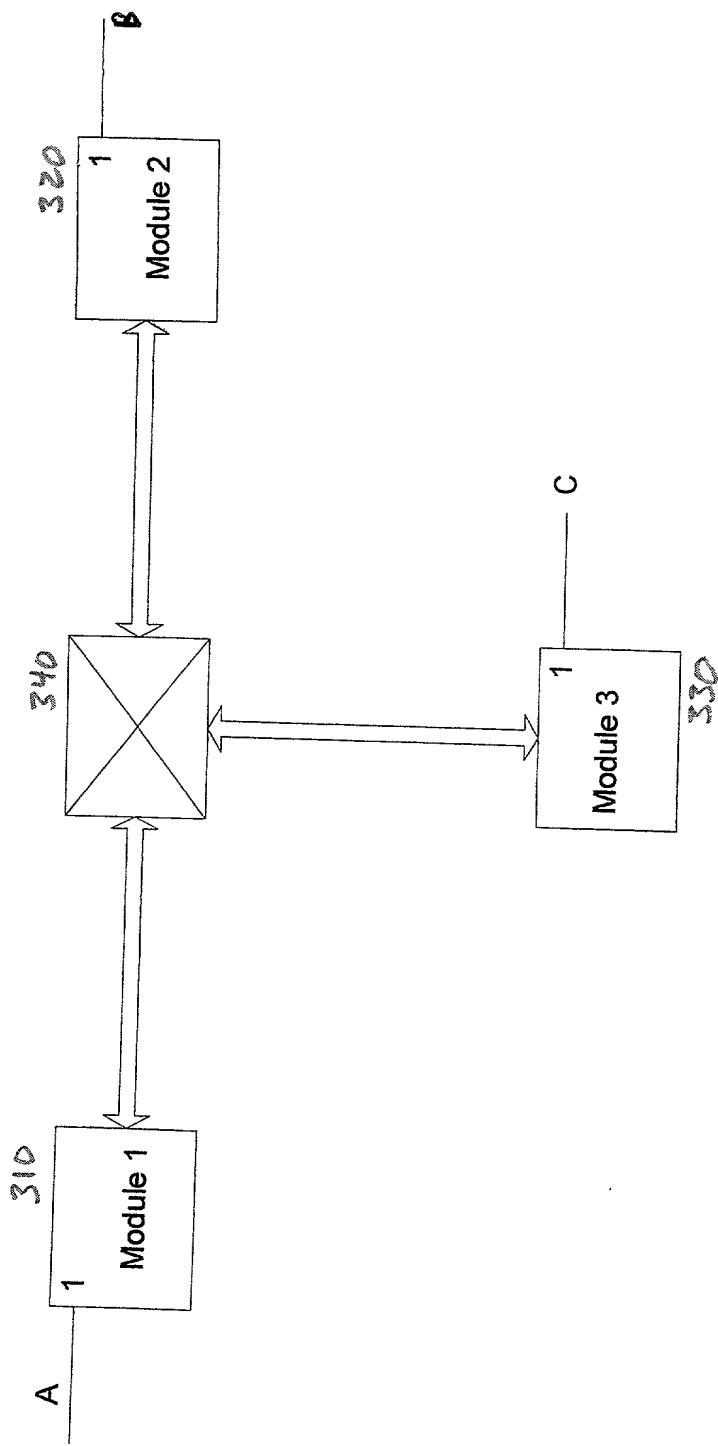


FIG. 3

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
E	R	L	F	F	NB	Priority	PF	Src_Port/SRC_	Src_Modid	M	Egress Port	Opcode/C			
H			T				M	TGID							
O			D												
Module Id Bitmap															
Data															
CRC (Module Header + Data)															

FIG. 4A

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
E	R	L	F	F	EZ	Priority	PF	Src_Port/SRC_	Src_Modid	M	Egress Port	Opcode/C			
H			T				M	TGID							
O			D												
Module Id Bitmap															
FPP Label															
Len															
Data															
CRC (Module Header + Data)															

FIG. 4B

VLAN Id	MAC Address	Module	Port	L_State

FIG- 5

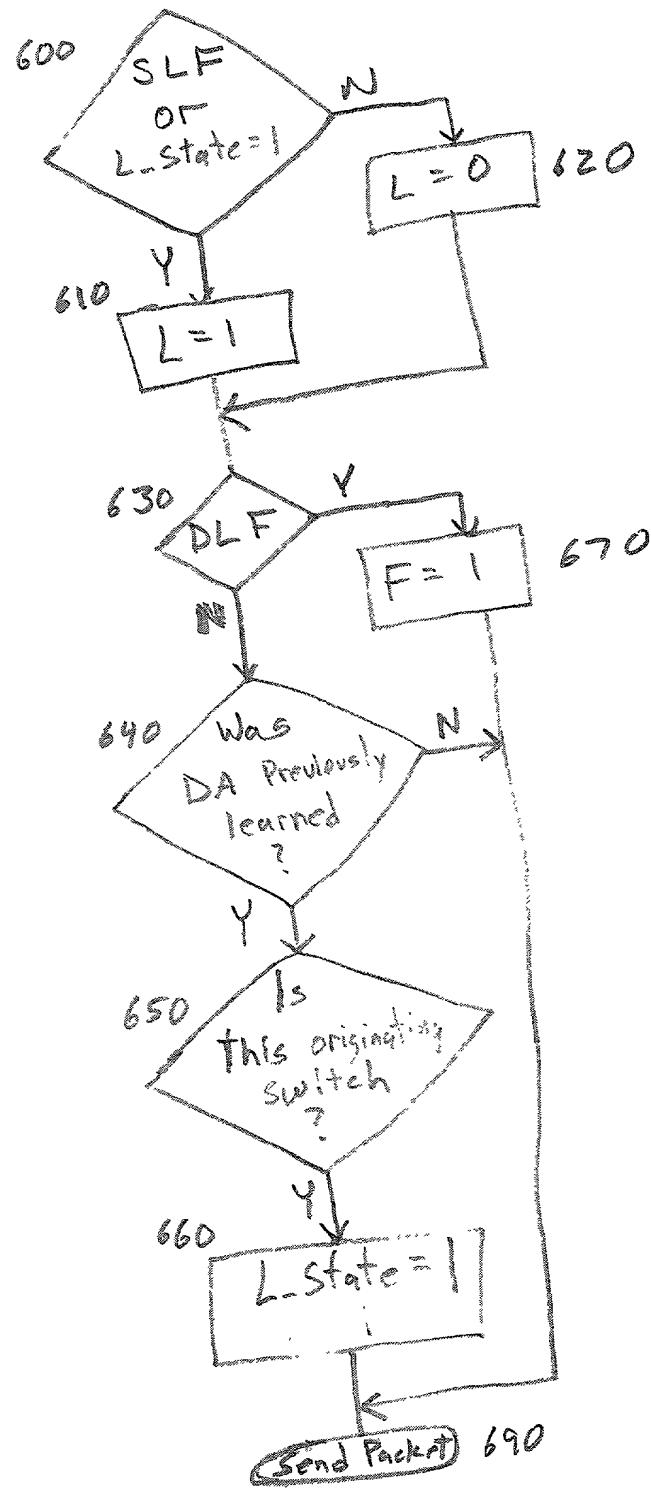


FIG. 6